



**DCEO (D.C.)**



**DEPARTMENT OF ELECTRONICS ENGINEERING  
MIT CAMPUS, ANNA UNIVERSITY, CHENNAI- 44**

**PG - End Semester Examination- November/December 2021**

**Semester: M.E. 1/4 (Regular)**

**Session: 9.30 AM – 12.30 PM (FN)**

**Regulation: R 2019**

Sl. No.	Date	Communication and Networking	Wireless Technologies	VLSI Design and Embedded Systems
1	20.05.2022 FN	-----	-----	VE5104 Real Time Embedded System Design
2	23.05.2022 FN	RM5151 Research Methodology and IPR	RM5151 Research Methodology and IPR	RM5151 Research Methodology and IPR
3	25.05.2022 FN	-----	-----	VE5102 VLSI Architecture for System Design
4	27.05.2022 FN	NE5103 High Performance Computer Networks	WT5102 Wireless Communication Techniques	-----
5	30.05.2022 FN	MA5155 Applied Mathematics for Network Engineers	MA5155 Applied Mathematics for Network Engineers	MA5159 Advanced Applied Mathematics
6	02.06.2022 FN	NE5101 Digital Communication Techniques	WT5151 Wireless Sensor Network Design	VE5101 Digital Integrated Circuit Design
7	06.06.2022 FN	NE5102 Advanced Optical Communication	WT5101 Wireless Broadband Networks	-----
8	08.06.2022 FN	-----	-----	VE5103 Advanced Embedded System Design
9	10.06.2022 FN	NE5151 RF Engineering	NE5151 RF Engineering	-----

Chief Superintendent (PG Examinations)

(S. Piramasubramanian)

Head of the Department

Head of the Department  
Department of Electronics Engineering  
MIT CAMPUS, ANNA UNIVERSITY  
CHROMEPET, CHENNAI-600 044.

**APPROVED**

17.5.22