

DEPARTMENT OF ELECTRONICS ENGINEERING, MIT CAMPUS, ANNA UNIVERSITY

M.E (F.T) END SEMESTER EXAMINATIONS TIME TABLE – APRIL / MAY 2022

ARREAR / SUPPLEMENTARY EXAMINATION

Timing - 09.30 a.m to 12.30 p.m

Date	Day	M.E - Communication and Networking			M.E – VLSI Design and Embedded Systems		
		Subject		Semester	Subject		Semester
		Code	Name		Code	Name	
18/07/2022	Monday	NE5077	Microwaves and Radar	Arrear	-	-	-
22/07/2022	Friday	-	-	-	VE5251	CMOS Analog IC Design	Arrear

APPROVED

[Signature]

Chief Superintendent
(PG Examination – Electronics, MIT)

16/6/22
M.E (F.T)



[Signature]
8/7/22

[Signature]

Head of Department
(Electronics, MIT)
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